

S/N 10/813,461

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vicki W. Tsai et al. Examiner: Unknown
Serial No.: 10/813,461 Group Art Unit: 2825
Filed: March 30, 2004 Docket: 80107.116US1
Title: BLOCK-BASED PROCESSING IN A PACKET-BASED RECONFIGURABLE ARCHITECTURE

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Applicant is resubmitting new copies of the three Foreign Patent Documents as well as the NPL document titled "A New Distributed DSP Architecture Based on the Intel IXS for Wireless Client and Infrastructure" cited in the IDS submitted on February 22, 2006 in compliance with the Office Action mailed on August 25, 2006, as the Examiner deemed them to be non-legible.

It is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

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Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

VICKI W. TSAI ET AL.

By their Representatives,

Customer No.: 45445

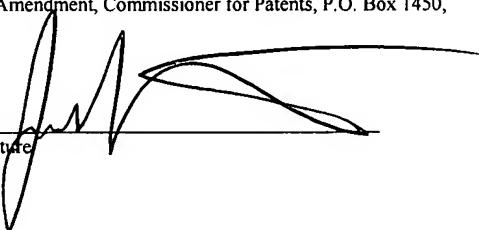
952-473-8800

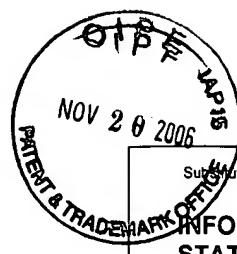
Date November 15, 2006

By Dana B. LeMoine
Dana B. LeMoine
Reg. No. 40,062

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 15 day of November, 2006.

Jenny Hollingsworth


Signature



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(Use as many sheets as necessary)</small>					
				<i>Complete if Known</i>	
				Application Number	10/813,461
				Filing Date	March 30, 2004
				First Named Inventor	Tsai, Vicki
				Art Unit	2825
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket No: 80107.116US1	

US PATENT DOCUMENTS

Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
		US-7073159	07/04/2006	Tsai, V. W., et al.	03/31/2004

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
		EP-1315335A1	05/28/2003	Cimini, Jr., L. J., et al.	
		WO-05098685A2	10/20/2005	Tsai, V. , et al.	
		WO-2005098685A3	10/20/2005	Tsai, V. , et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		BENINI, L. , et al., "System-Level Power Optimization: Techniques and Tools", <u>ACM Transactions on Design Automation of Electronic Systems</u> , 5(2), (Apr. 2000),115-192	
		TSUI, E. , et al., "A New Distributed DSP Architecture Based on the Intel IXS for Wireless Client and Infrastructure", <u>Hot chips 14, Conf. record Session 7:digital signal processors</u> , 20 Aug.2002, (2002),1-17	

EXAMINER

DATE CONSIDERED